**Mapping CPU addresses to required fields**

32 18 17 10 9 8 7 6 5 4 3 2 1 0

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| Row | High column | Bank | Bank group | Low column | Byte Order |

* Lower 3 LSB bits used to select byte order.
* To access burst of 8, low column bits are used which are 3 bits.
* Next to utilize the bank parallelism, we have to make sure that the next reference goes to a different open bank in a parallel bank group; so considering 4 bank group, we use next 2 bits for bank groups.
* Then 2 bits to select the banks from above selected bank group.
* Assuming 11 total columns and using lower column bits for column selection, we use the remaining 8 bits.
* The 15 rows are accessed next.